

**Amendments to the Claims:**

1. (original) A method for transmitting data in a multi-chip system, the multi-chip system comprising at least a host chip and at least a slave chip, the method comprising the following steps:

- 5 (a) the slave chip informing the host chip of data needed to be transmitted;  
(b) when being informed by the slave chip, the host chip informing the slave chip to start to transmit the data; and  
(c) when being informed by the host chip, the slave chip starting to transmit the data to the host chip.

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2. (original) The method of claim 1 wherein in step (b) the host chip further delivers a clock signal to the slave chip.

15 3. (original) The method of claim 1 wherein in step (a) the slave chip actively alters a voltage on a request pin pair, electrically connected between the host chip and the slave chip, to inform the host chip of the data needed to be transmitted.

20 4. (original) The method of claim 1 wherein in step (a) the slave chip detects states of a plurality of signals, when any changes of the states of the plurality of the signals are detected, the slave chip actively alters a voltage on a request pin pair to inform the host chip of the data needed to be transmitted, wherein the request pin pair is electrically connected between the host chip and the slave chip.

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5. (original) The method of claim 1 wherein in step (b) the host chip detects a voltage on a request pin pair, when the host chip detects that the voltage on the request pin pair has changed, the host chip delivers a clock

signal to the slave chip via a clock pin pair, wherein the request pin pair and the clock pin pair are both electrically connected between the host chip and the slave chip.

5     6. (original) The method of claim 1 wherein in step (b) the host chip alters a voltage on a latch pin pair for informing the slave chip to start transmitting the data, wherein the latch pin pair is electrically connected between the host chip and the slave chip.

10    7. (original) The method of claim 1 wherein in step (c) the slave chip transmits the data to the host chip via a data pin pair on a basis of a clock signal of a clock pin pair, wherein the data pin pair and the clock pin pair are both electrically connected between the host chip and the slave chip.

15    8. (original) The method of claim 1 wherein in step (c) the slave chip transmits states of a plurality of signals to the host chip via a data pin pair on a basis of a clock signal of a clock pin pair, wherein the data pin pair and the clock pin pair are both electrically connected between the host chip and the slave chip.

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9. (original) The method of claim 1 wherein the method further comprises the following step:

(d) the host chip receiving data from the slave chip and decoding the data.

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10. (new) The method of claim 1 wherein the slave chip is an analog chip and the host chip is a digital chip.

11. (new) The method of claim 1 wherein the multi-chip system is an optical disk drive.

12. (new) The method of claim 11 wherein the slave chip is a servo control  
5 chip and the host chip is for controlling operations of the optical disk drive.

13. (new) The method of claim 11, wherein in step (b), when being informed by the slave chip, the host chip further for delivering a clock  
10 signal to the slave chip, and when not being informed by the slave chip, the host chip not delivering the clock signal to the slave chip.

14. (new) The method of claim 13 wherein in step (b) the host chip further delivering the clock signal to the slave chip having a predetermined  
15 number of clock cycles.

15. (new) The method of claim 14 wherein in step (c) when being informed by the host chip, the slave chip transmitting a fixed number of servo signals to the host chip; wherein the fixed number is equal to the  
20 predetermined number and one servo signal is transmitted by the slave chip to the host chip during each clock cycle.

16. (new) The method of claim 15 wherein at least one of the servo signals is a tracking servo signal.

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17. (new) The method of claim 15 wherein at least one of the servo signals is a focusing servo signal.

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18. (new) The method of claim 15 wherein at least one of the servo signals is a tray open signal.

19. (new) The method of claim 15 wherein at least one of the servo  
5 signals is a tray close signal.

20. (new) The method of claim 15 wherein at least one of the servo signals is a disk blank signal.

10 21. (new) The method of claim 15 wherein at least one of the servo signals is a disk defect signal.